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AO3485C

20V P-Channel MOSFET

General Description

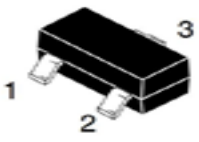
- Trench Power MOSFET technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- RoHS and Halogen-Free Compliant

Applications

- This device is ideal for Load Switch

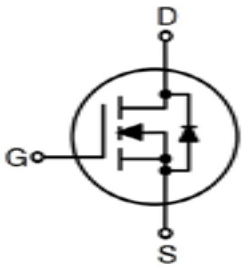
Product Summary

V_{DS}	-20V
I_D (at V_{GS}=-4.5V)	-4.5A
$R_{DS(ON)}$ (at V_{GS}=-4.5V)	< 40mΩ
$R_{DS(ON)}$ (at V_{GS}=-2.5V)	< 55mΩ
$R_{DS(ON)}$ (at V_{GS}=-1.8V)	< 72mΩ
Typical ESD protection	**HBM Class 2**



SOT-23

1 – Gate
2 – Source
3 – Drain



Absolute Maximum Ratings T_A=25°C unless otherwise noted

Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	±8	V
Continuous Drain Current	T_A=25°C	I_D	-4.5	A
	T_A=70°C		-3.5	
Pulsed Drain Current [C]		I_{DM}	-18	
Power Dissipation [B]	T_A=25°C	P_D	1.3	W
	T_A=70°C		0.8	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient [A]	t ≤ 10s	$R_{\theta JA}$	70	90	°C/W
Maximum Junction-to-Ambient [A D]	Steady-State		100	125	°C/W
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	63	80	°C/W

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Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	I_D=-250μA, V_{GS}=0V	-20			V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-20V, V_{GS}=0V			-1	μA
		T_J=55°C			-5	
I_{GSS}	Gate-Body leakage current	V_{DS}=0V, V_{GS}=±8V			±10	μA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=$V_{GS,}$ I_D=-250μA	-0.15	-0.55	-0.95	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	V_{GS}=-4.5V, I_D=-4.5A		33	40	mΩ
		T_J=125°C		43	52	
		V_{GS}=-2.5V, I_D=-4A		42	55	mΩ
		V_{GS}=-1.8V, I_D=-3.5A		54	72	mΩ
g_{FS}	Forward Transconductance	V_{DS}=-5V, I_D=-4.5A		20		S
V_{SD}	Diode Forward Voltage	I_S=-1A, V_{GS}=0V		-0.7	-1	V
I_S	Maximum Body-Diode Continuous Current				-2	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=-10V, f=1MHz		930		pF
C_{oss}	Output Capacitance			90		pF
C_{rss}	Reverse Transfer Capacitance			80		pF
R_g	Gate resistance	f=1MHz		15	30	Ω
SWITCHING PARAMETERS						
Q_g(4.5V)	Total Gate Charge	V_{GS}=-4.5V, V_{DS}=-10V, I_D=-4.5A		8.5	17	nC
Q_{gs}	Gate Source Charge			1		nC
Q_{gd}	Gate Drain Charge			2.5		nC
$t_{D(on)}$	Turn-On DelayTime	V_{GS}=-4.5V, V_{DS}=-10V, R_L=2.22Ω, R_{GEN}=3Ω		12		ns
t_r	Turn-On Rise Time			11		ns
$t_{D(off)}$	Turn-Off DelayTime			82		ns
t_f	Turn-Off Fall Time			35		ns
t_{rr}	Body Diode Reverse Recovery Time	I_F=-4.5A, di/dt=500A/μs		25		ns
Q_{rr}	Body Diode Reverse Recovery Charge	I_F=-4.5A, di/dt=500A/μs		37		nC

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

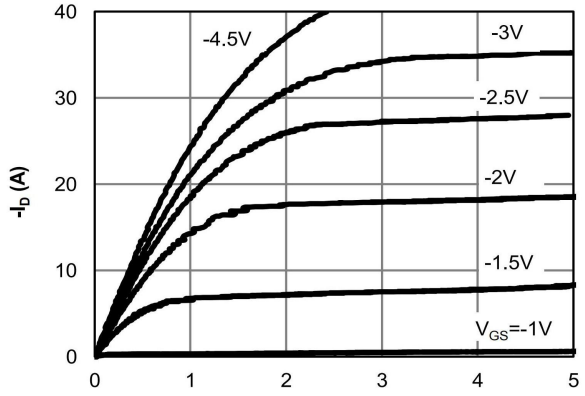


Figure 1: On-Region Characteristics (Note E)

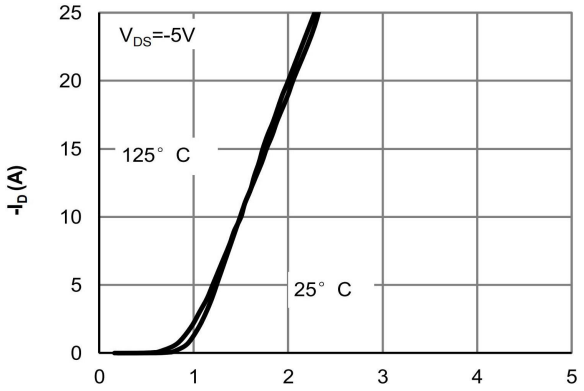


Figure 2: Transfer Characteristics (Note E)

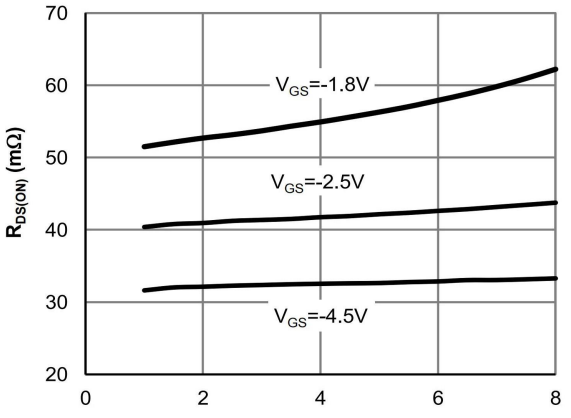


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

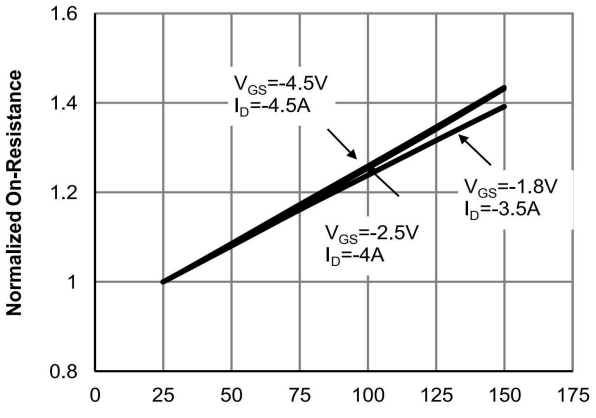


Figure 4: On-Resistance vs. Junction Temperature (Note E)

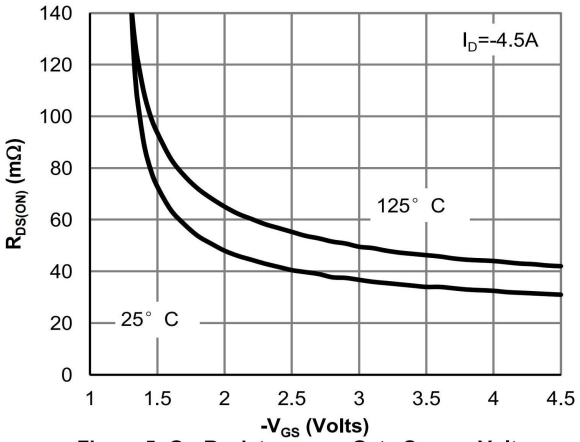


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

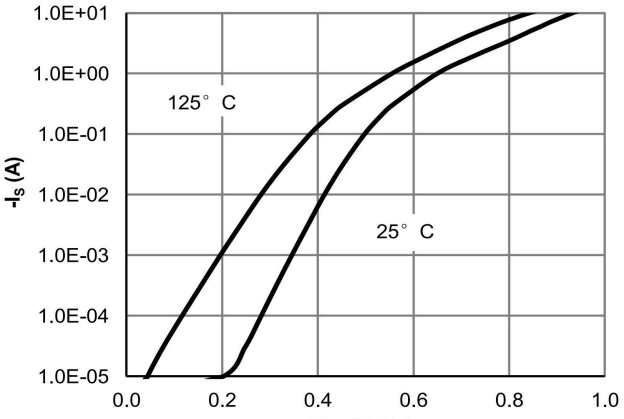


Figure 6: Body-Diode Characteristics (Note E)

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

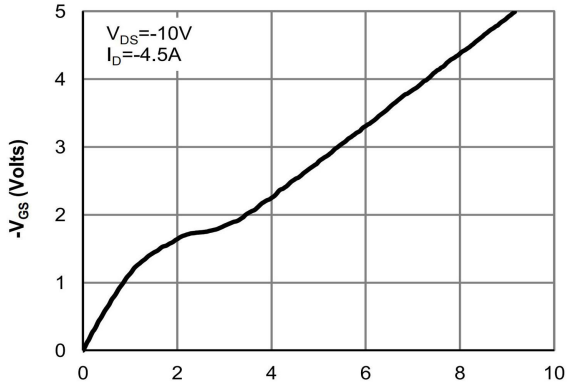


Figure 7: Gate-Charge Characteristics

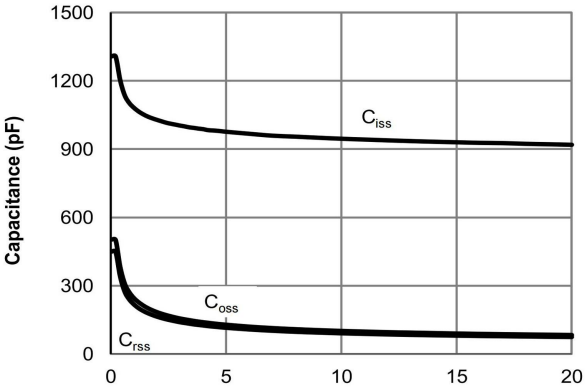
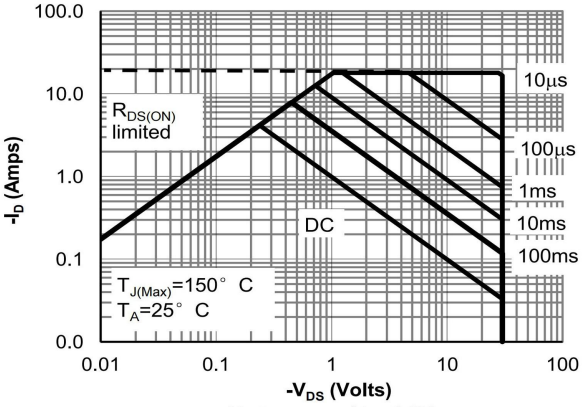


Figure 8: Capacitance Characteristics



V_{GS}> or equal to -1.8V
Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

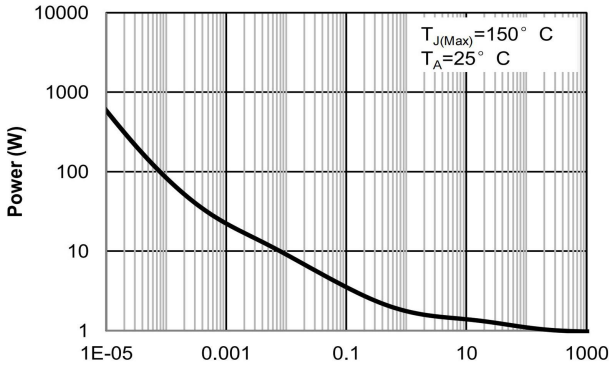


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

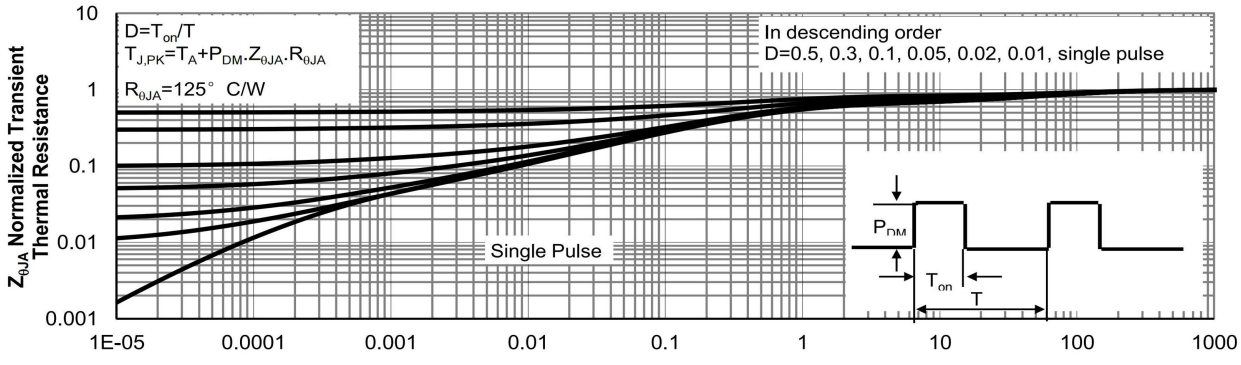


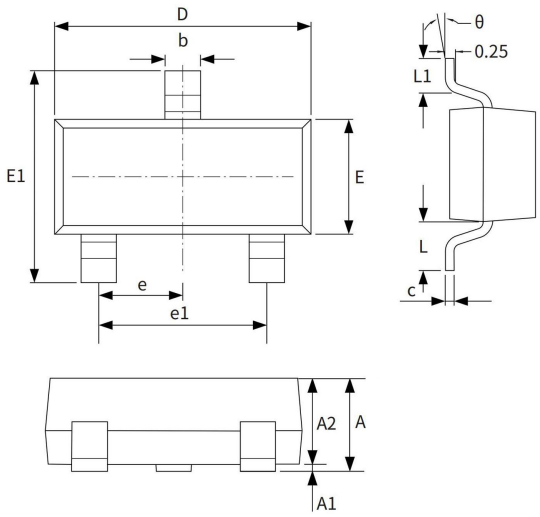
Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

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Ordering Information

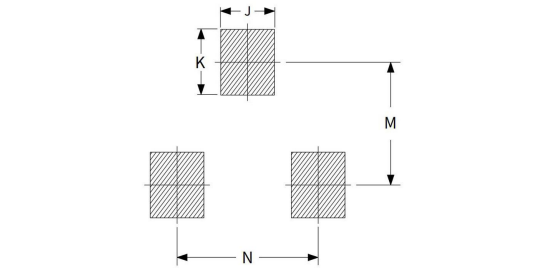
PACKAGE	PACKAGE CODE	UNIT WEIGHT(g)	REEL(pcs)	BOX(pcs)	CARTON(pcs)	DELIVERY MODE
SOT-23	R1	0.008	3000	30000	240000	7''

Package Outline Dimensions (SOT-23)



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.90	1.15	0.035	0.045
A1	-	0.10	-	0.004
A2	0.90	1.05	0.035	0.041
b	0.30	0.50	0.012	0.020
c	0.10	0.20	0.004	0.008
D	2.80	3.00	0.110	0.118
E	1.20	1.40	0.047	0.055
E1	2.25	2.55	0.089	0.100
e	0.950TYP		0.037TYP	
e1	1.80	2.00	0.071	0.079
L	0.550REF		0.022REF	
L1	0.30	0.50	0.012	0.020
θ	-	8°	-	8°

Suggested Pad Layout



Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
J	0.75	0.85	0.030	0.033
K	0.85	0.95	0.033	0.037
M	1.95	2.05	0.077	0.081
N	1.85	1.95	0.073	0.077