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H7VN10B

Bi-directional 7V Normal Capacitance ESD

DFN1006-2L Plastic-Encapsulate ESD Protection Diodes

Features

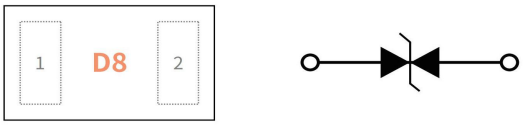
- Low leakage current
- DFN1006 surface mount package
- IEC 61000-4-2 (ESD Air): ±30kV
- IEC 61000-4-2 (ESD Contact): ±30kV
- IEC 61000-4-5 (Lightning 8/20μs): 6.0A

Reverse Working Voltage
7V Max.
Normal capacitance
22pF(Max.)

Applications

- Cellular Handsets and Accessories
- Personal Digital Assistants
- Notebooks and Handhelds
- Portable Instrumentation,Digital Cameras
- Peripherals, Audio Players, Industrial Equipment
- Keypads, Side Keys, LCD Displays

Function Diagram



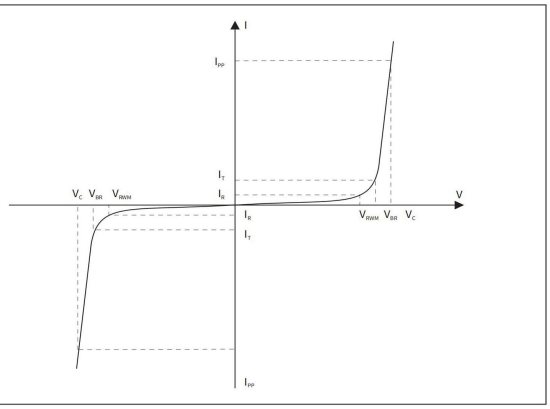


Maximum Ratings (Ta=25°C Unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{ESD}	Electrostatic Discharge Voltage	ESD per IEC 61000-4-2(Air)	±30	KV
		ESD per IEC 61000-4-2(Contact)	±30	KV
P_{PP}	Peak Pulse Power	tp = 8/20 μs	80	W
I_{PP}	Rated Peak Pulse Current	tp = 8/20 μs	6.0	A
T_J	Operating JunctionTemperature Range	—	-55 to +125	°C
T_{STG}	Operating JunctionTemperature Range	—	-55 to +150	°C

Electrical Parameter

SYMBOL	PARAMETER
V_C	Clamping Voltage @ I_{PP}
V_{BR}	Breakdown Voltage @ I_T
I_{PP}	Peak Pulse Current
I_T	Test Current
I_R	Reverse Leakage Current @ VRWM
V_{RWM}	Peak Reverse Working Voltage
P_{PP}	Peak Pulse Power Dissipation
C_J	Junction Capacitance @ V_R=0V,f=1MHz
I_F	Forward Current
V_F	Forward Voltage @I_F



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Electrical Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	CONDITION	Min	Typ	Max	UNIT
Peak Reverse Working Voltage	V_{RWM}	T_a=25°C	—	—	7.0	V
Breakdown Voltage	V_{BR}	I_R=1.0mA,T_a=25°C	7.5	—	9.6	V
Reverse Leakage Current	I_R	V_R=7.0V,T_a=25°C	—	—	0.2	μA
Clamping Voltage	V_C	I_{PP}=1.0A, t_p=8/20μs	—	—	9.0	V
		I_{PP}=6.0A,t_p=8/20μs	—	—	14	
Junction Capacitance	C_J	V_R=0V,f=1MHz	—	15	22	pF

Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)

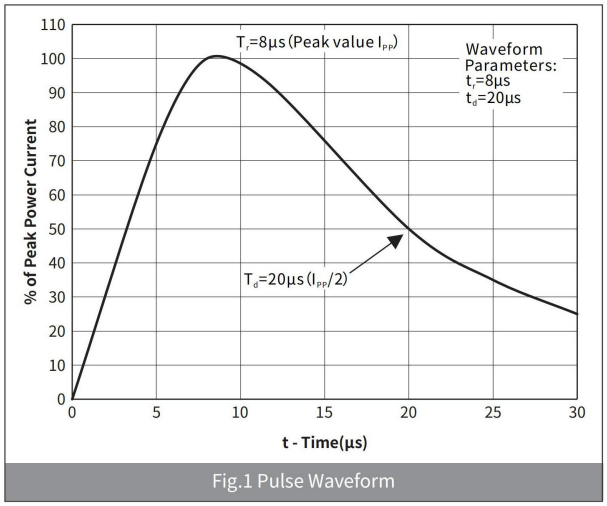


Fig.1 Pulse Waveform

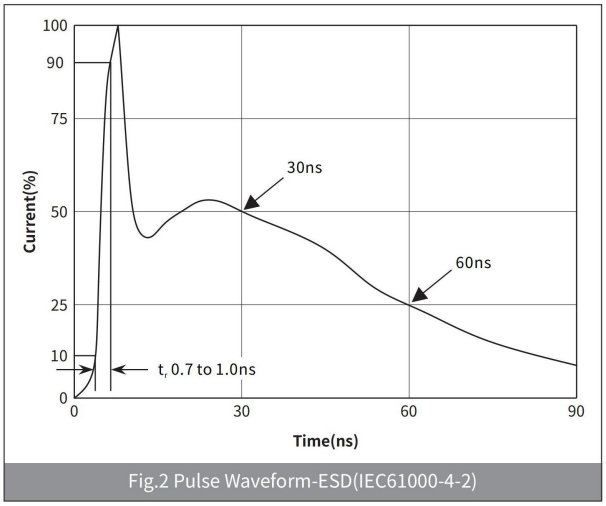


Fig.2 Pulse Waveform-ESD(IEC61000-4-2)

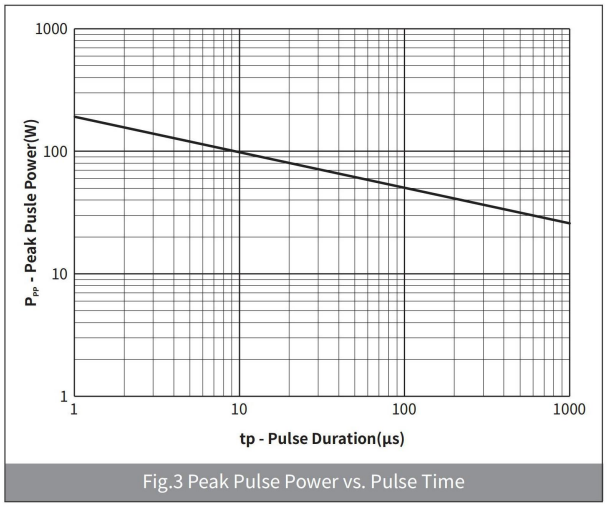


Fig.3 Peak Pulse Power vs. Pulse Time

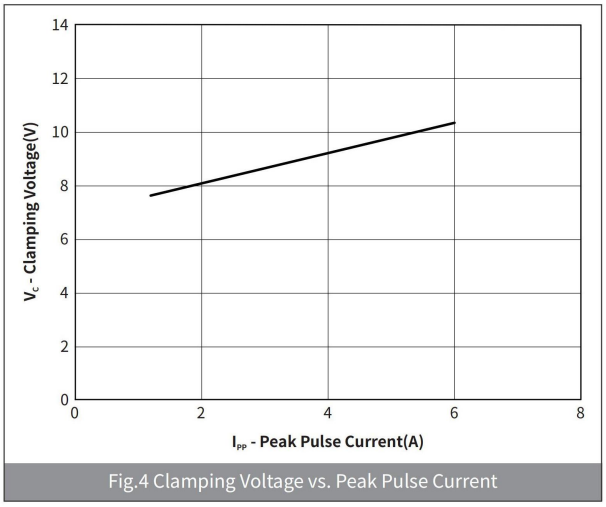


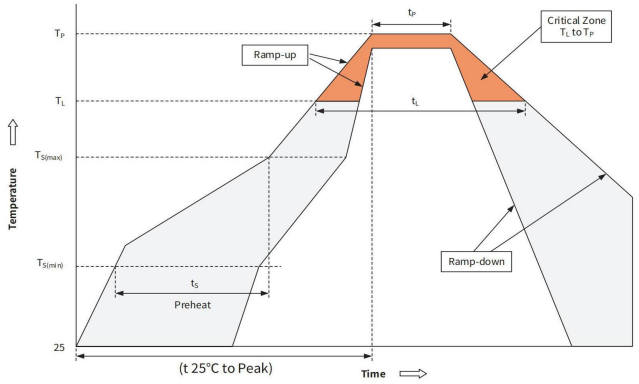
Fig.4 Clamping Voltage vs. Peak Pulse Current

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Ordering Information

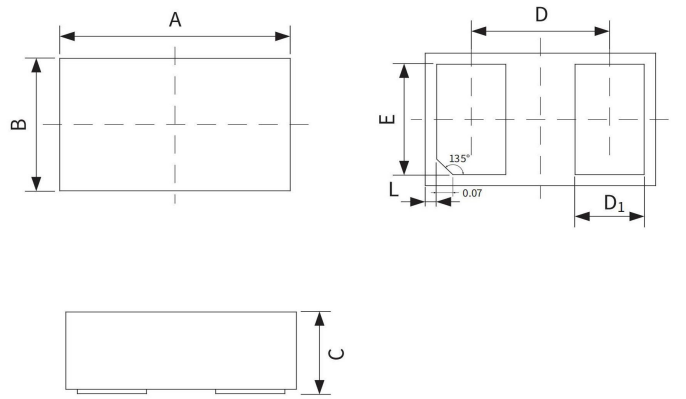
PREFERED P/N	PACKAGE	SIZE(mm)	DELIVERY MODE	MPQ(PCS)
	DFN1006-2L	1.00×0.60×0.37	7'' REEL	10,000

Recommended Soldering Conditions



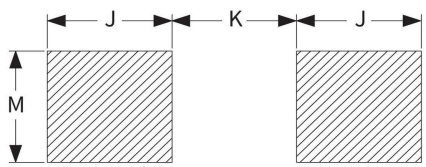
Profile Feature		Pb-Free Assembly
Pre-heat	Temperature Min ($T_{S(min)}$)	+150°C
	Temperature Max($T_{S(max)}$)	+200°C
	Time (Min to Max) (t_S)	60-180 secs.
Average ramp up rate (Liquid us Temp (T_L) to peak)		3°C /sec. Max
$T_{S(max)}$ to T_L - Ramp-up Rate		3°C /sec. Max
Reflow	Temperature(T_L)(Liquid us)	+217°C
	Temperature(t_L)	60-150 secs.
Peak Temp (T_P)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t_P)		20-40secs
Ramp-down Rate		6°C /sec. Max
Time 25°C to Peak Temp (T_P)		8 min. Max
Do not exceed		+260°C

Package Outline Dimensions (DFN1006-2L)



Symbol	Dimensions			
	Millimeters		Inches	
	Min	Max	Min	Max
A	0.950	1.050	0.037	0.041
B	0.550	0.650	0.022	0.026
C	0.350	0.450	0.014	0.018
D	0.550	0.650	0.022	0.026
D_1	0.280	0.380	0.011	0.015
E	0.450	0.550	0.018	0.022
L	0.000	0.100	0.000	0.004

Suggested Pad Layout



Symbol	Dimensions			
	Millimeters		Inches	
	Min	Max	Min	Max
J	0.50	0.60	0.020	0.024
K	0.25	0.35	0.010	0.014
M	0.55	0.65	0.022	0.026